

**RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
GROUP ART UNIT 2818**

Docket Number  
40404.30/tt

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hiroshi OJI  Application No.: 10/572,730  Confirmation No.: 6903  Filing or 371(c) Date: March 21, 2006  Title: SEMICONDUCTOR DEVICE HAVING HIGH-K GATE DIELECTRIC LAYER AND METHOD FOR MANUFACTURING THE SAME	Art Unit: 2818  Examiner: A. Huynh
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**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 10, 2009, the period for response to which has been extended to November 10, 2009 by the accompanying Petition for a ONE-month Extension of Time, please amend the above-identified application as follows:

☐ **Amendments to the Specification** begin on page       of this paper.

☒ **Amendments to the Claims** are reflected in the listing of the claims which begins on page 2 of this paper.

☐ **Amendments to the Drawings** begin on page       of this paper and include an attached replacement sheet. An **Appendix** including the amended drawing figures is attached following page       of this paper.

☒ **Remarks/Arguments** begin on page 4 of this paper.

Please note, if a box is not checked, then no corresponding amendment is being made.